a gate electrode protruding from said gate line in an area where said data line crosses said gate line,

a thin film transistor having a source electrode connected to the data line and a drain electrode separated from the source electrode wherein the source and drain electrodes confront each other,

a passivation layer covering the thin film transistor wherein a contact hole exposing a portion of the drain electrode is formed in the passivation layer, and

a pixel electrode formed on the passivation layer and being connected to the drain electrode through the contact hole, wherein the pixel electrode partially overlaps the data line;

a color filter plate including a black matrix, a color filter and a common electrode on a second transparent substrate; and

liquid crystals injected and sealed between the thin film transistor plate and the color filter plate,

wherein the black matrix of the color filter plate asymmetrically overlaps the data line of the thin film transistor plate.

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6. (Amended) A liquid crystal display comprising:

a thin film transistor plate further comprising:

a gate line on a first transparent substrate,

a first data line arranged to cross the gate line wherein the gate line is insulated from the data line,

a gate electrode protruding from said gate line in an area where said data line crosses said gate line,

a thin film transistor having a source electrode connected to the first data line and a drain electrode separated from the source electrode wherein the source and drain electrodes confront each other,

a passivation layer covering the thin film transistor wherein a contact hole exposing a portion of the drain electrode is formed in the passivation layer, and

a pixel electrode formed on the passivation layer and being connected to the drain electrode through the contact hole, wherein the pixel electrode partially overlaps the first data line at a first end of the pixel electrode;

a color filter plate including a black matrix, a color filter and a common electrode on a second transparent substrate; and

liquid crystals injected and sealed between the thin film transistor plate and the color filter plate,

wherein the pixel electrode asymmetrically overlaps a second data line at a second end of the pixel electrode opposite the first end.

10. (Amended) A liquid crystal display comprising:

a thin film transistor plate further comprising:

a gate line on a first transparent substrate,

a data line arranged to cross the gate line wherein the gate line is insulated from the data line,

a gate electrode protruding from said gate line in an area where said data line crosses said gate line,

a thin film transistor having a source electrode connected to the data line and a drain electrode separated from the source electrode wherein the source and drain electrodes confront each other;

A3 end

a passivation layer covering the thin film transistor wherein a contact hole exposing a portion of the drain electrode is formed in the passivation layer; and

a pixel electrode formed on the passivation layer and being connected to the drain electrode through the contact hole, wherein the pixel electrode partially overlaps the data line;

a color filter plate including a black matrix, a color filter and a common electrode on a second transparent substrate; and

liquid crystals injected and sealed between the thin film transistor plate and the color filter plate,

wherein a cut-off film is formed under the data line, said cut-off film being asymmetrically overlapped by the data line and being partially overlapped by the pixel electrode.